

# FAULT CURRENT LIMITER

## Abstract of Disclosure

A fault current limiter (FCL) comprises: an air core flat clock spiral inductor comprising wound electrically conductive material and insulated turns; and two terminations configured for attaching the spiral inductor in series with a power carrying conductor.

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Figures

Figure 1: A schematic diagram illustrating the proposed system architecture. The diagram shows a central processing unit (CPU) connected to a memory unit (RAM) and a storage unit (SSD). The CPU is also connected to a network interface (NIC) and a display unit (GPU). The storage unit is connected to the CPU via a SATA interface. The network interface is connected to the CPU via a USB interface. The display unit is connected to the CPU via a video interface. The diagram also shows a power supply unit (PSU) connected to the CPU, RAM, and storage unit. The diagram is labeled with various components and their connections, including CPU, RAM, SSD, NIC, GPU, SATA, USB, and PSU.